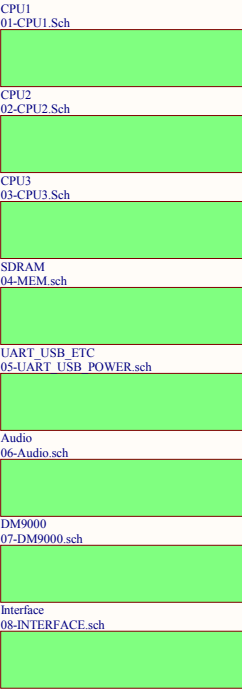


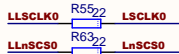
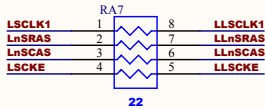
Schematic Diagram

mini2440

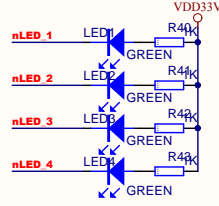
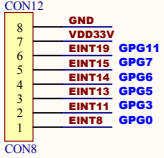
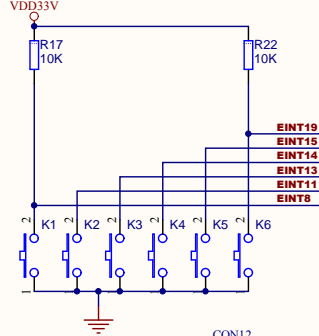
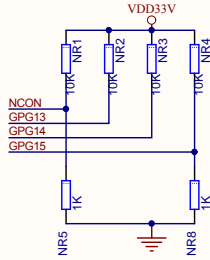
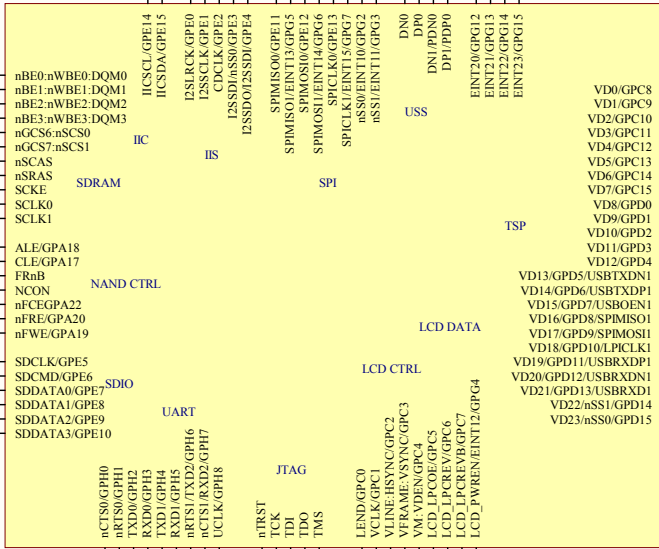


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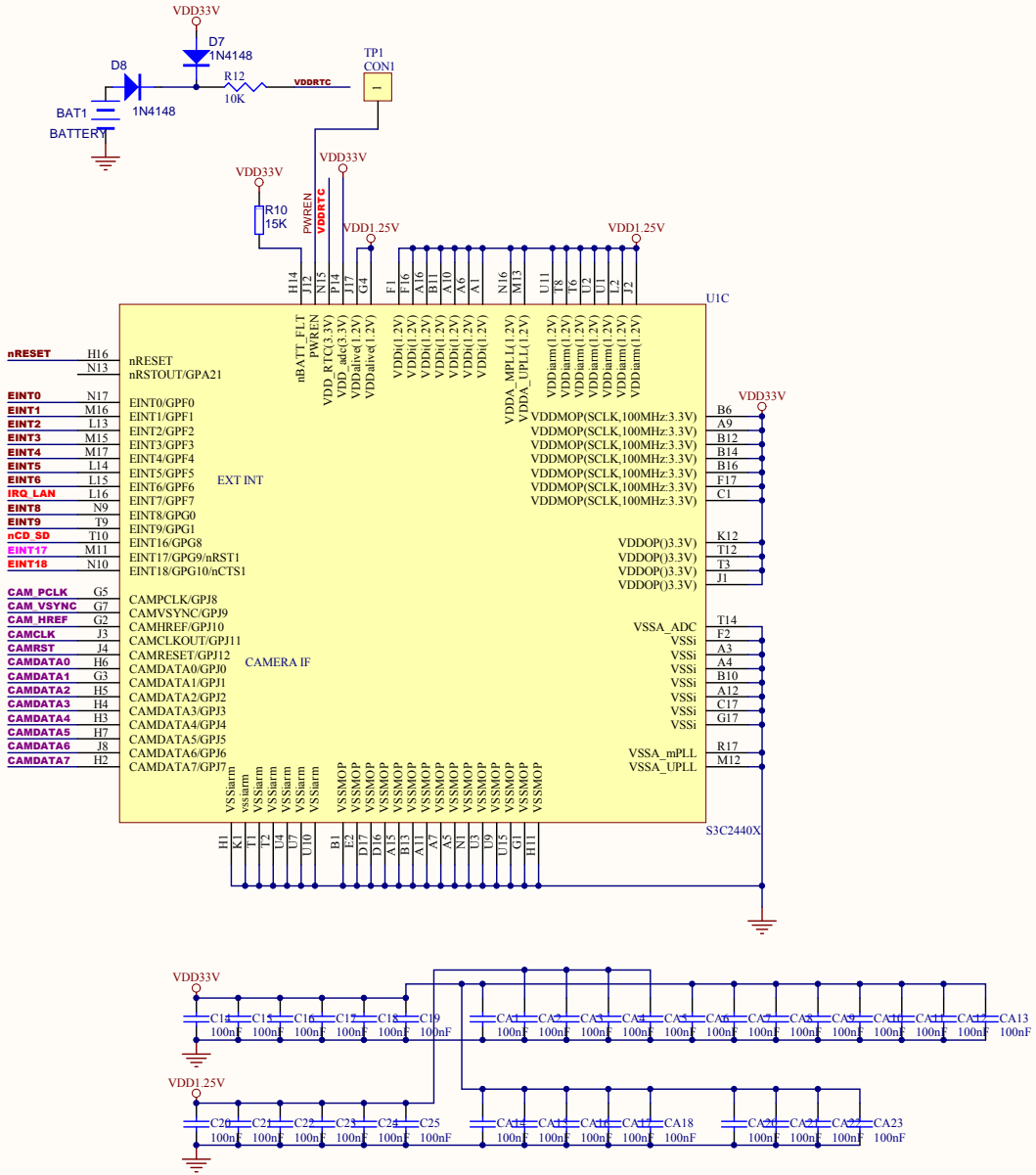
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FriendlyARM mini2440			
Size	Number	Translation by S. Watterott	Revision
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Date:	13.04.2009	Sheet	of
File:	D:\Dokumente und Einstellungen\Main	Drawn By:	



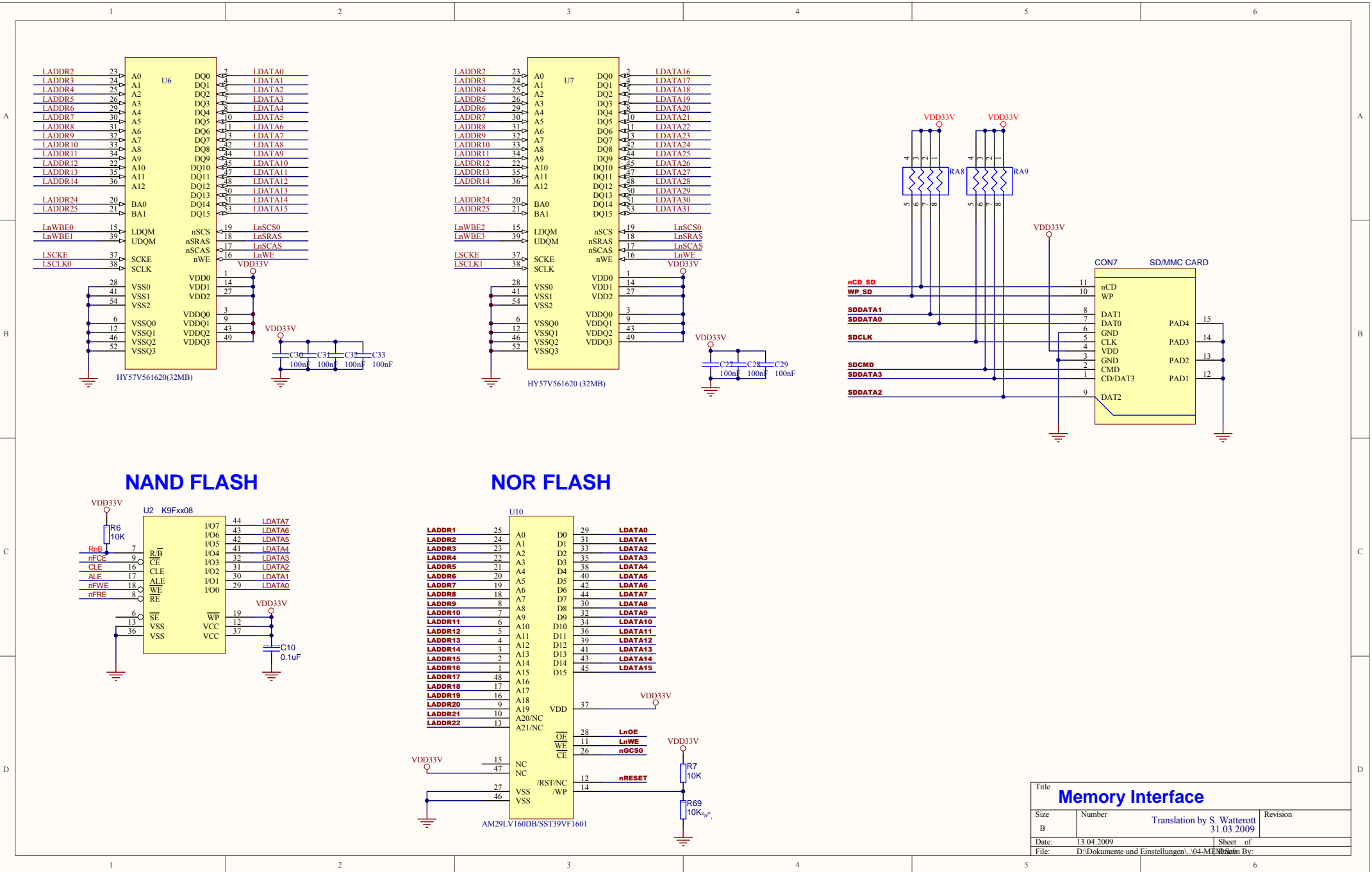
LnWBE0	D4
LnWBE1	B5
LnWBE2	D5
LnWBE3	E5
LLnSCS0	D2
	E3
LLnSCAS	D6
LLnSRAS	C6
LLSCKE	A2
LLSCLK0	B4
LLSCLK1	B3
ALE	D1
CLE	F5
RnB	G6
NCON	R12
nFCE	F4
nFRE	E1
nFWE	F3

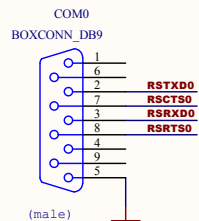
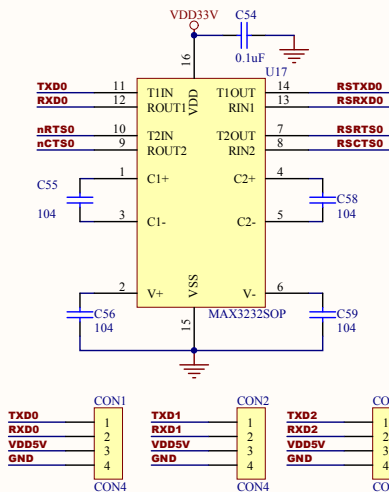


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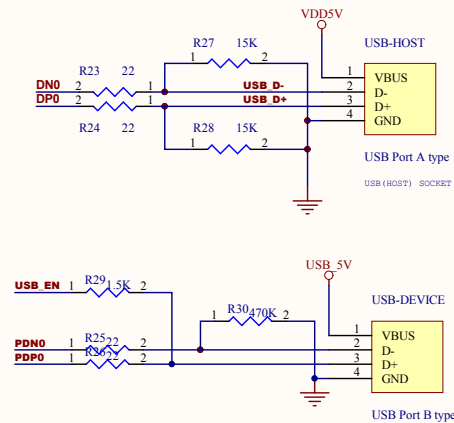


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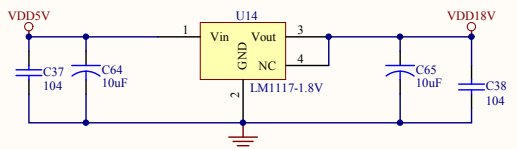


Serial Interface

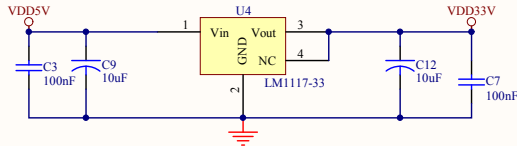


USB

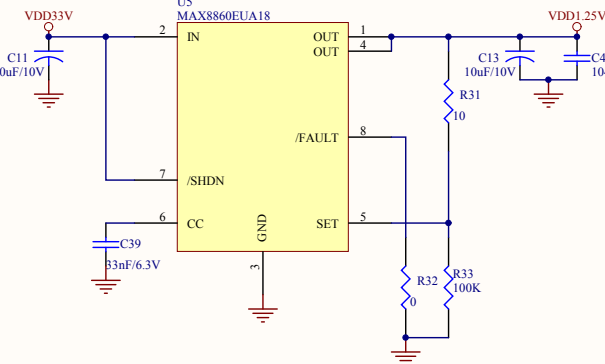
1.8 Volt regulator



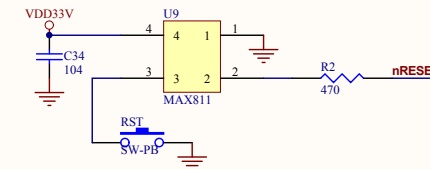
3.3V regulator



1.25 Volt regulator

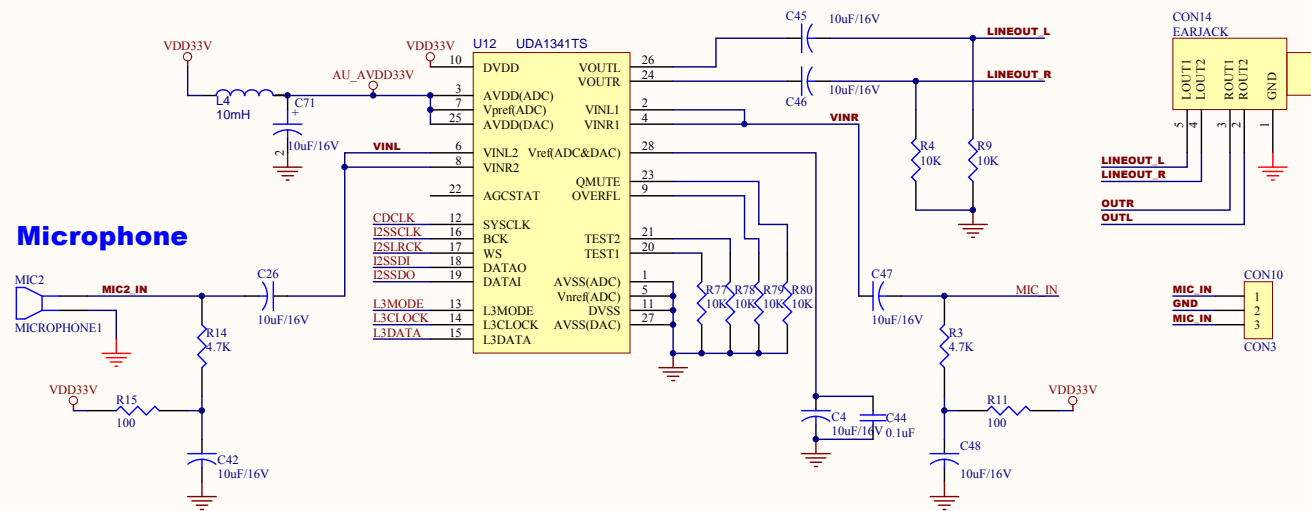


Reset

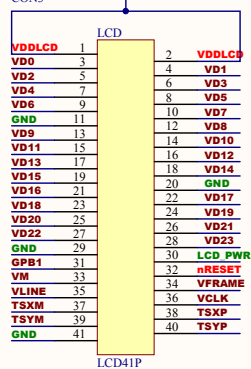


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UART / USB / Power			
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Date:	13.04.2009	Sheet of	
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Audio in- and output



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Audio Interface			
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B		31.03.2009	
Date:	13.04.2009	Sheet of	
File:	D:\Dokumente und Einstellungen\... \06-Audio	Drawn By:	



CONS			
VDD5V	1	2	GND
EINT17	3	4	EINT18
EINT3	5	6	EINT9
nGCS1	7	8	nGCS2
nGCS3	9	10	nGCS5
nGCS4	11	12	LoWE
nWAIT	13	14	nRESET
LoOE	15	16	nXDRREQ0
LADDR0	17	18	LADDR1
LADDR2	19	20	LADDR3
LADDR4	21	22	LADDR5
LADDR6	23	24	LADDR24
LDATA0	25	26	LDATA1
LDATA2	27	28	LDATA3
LDATA4	29	30	LDATA5
LDATA6	31	32	LDATA7
LDATA8	33	34	LDATA9
LDATA10	35	36	LDATA11
LDATA12	37	38	LDATA13
LDATA14	39	40	LDATA15

CMERA

Pin	Signal
1	I2CSDA
2	I2CSCL
3	EINT20
4	CAMRST
5	CAMCLK
6	CAM_HREF
7	CAM_VSYNC
8	CAM_PCLK
9	CAMDAT7
10	CAMDAT6
11	CAMDAT5
12	CAMDAT4
13	CAMDAT3
14	CAMDAT2
15	CAMDAT1
16	CAMDAT0
17	VDD33V
18	VDD_CAM
19	VDD18V
20	GND

CON20

CON4			
VDD5V	1	2	VDD33V
GND	3	4	nRESET
AIN0	5	6	AIN1
AIN2	7	8	AIN3
EINT0	9	10	EINT1
EINT2	11	12	EINT3
EINT4	13	14	EINT5
EINT6	15	16	EINT8
EINT9	17	18	EINT11
EINT13	19	20	EINT14
EINT15	21	22	EINT17
EINT18	23	24	EINT19
SPIMISO	25	26	SPIMOSI
SPICLK	27	28	nSS SPI
IC2C5CL	29	30	IC2C5DA
GPB0	31	32	GPB1
CLKOUT0	33	34	CLKOUT1

general int resist pulled up

The image shows two circuit diagrams for general interrupt pins. Both diagrams feature a 10k resistor (RA1 and RA2 respectively) connected to VDD33V. The left diagram shows pins EINT0, EINT1, EINT2, and EINT4 connected to the resistor. The right diagram shows pins EINT5, EINT6, CLKOUT0, and CLKOUT1 connected to the resistor.

<p>EINT11 1</p> <p>EINT13 2</p> <p>EINT14 3</p> <p>EINT15 4</p>	<p>RA3 10K</p>	<p>VDD33V</p> <p>7</p> <p>8</p> <p>5</p>	<p>SPICLK 1</p> <p>SPIMISO 2</p> <p>SPIMOSI 3</p> <p>nSS SPI 4</p>	<p>RA4 10K</p>	<p>VDD33V</p> <p>7</p> <p>8</p> <p>5</p>
<p>1</p> <p>2</p> <p>I2CSCL 3</p> <p>I2CSDA 4</p>	<p>RA5 10K</p>	<p>VDD33V</p> <p>7</p> <p>8</p> <p>5</p>	<p>EINT3 1</p> <p>EINT9 2</p> <p>EINT17 3</p> <p>EINT18 4</p>	<p>RA6 10K</p>	<p>VDD33V</p> <p>7</p> <p>8</p> <p>5</p>

i2c resist pulled up

system busl int resist pulled up

Title			Interface		
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